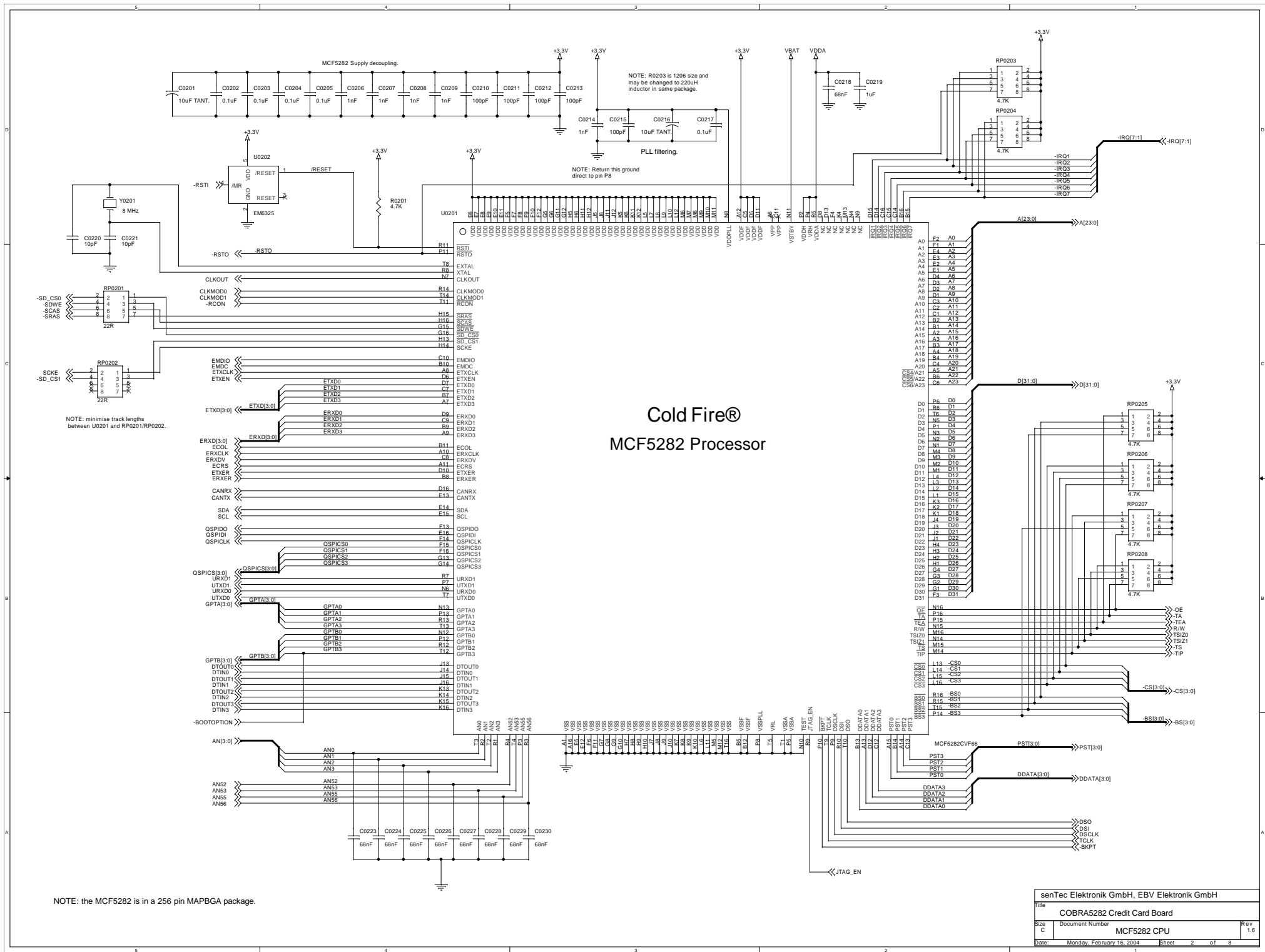
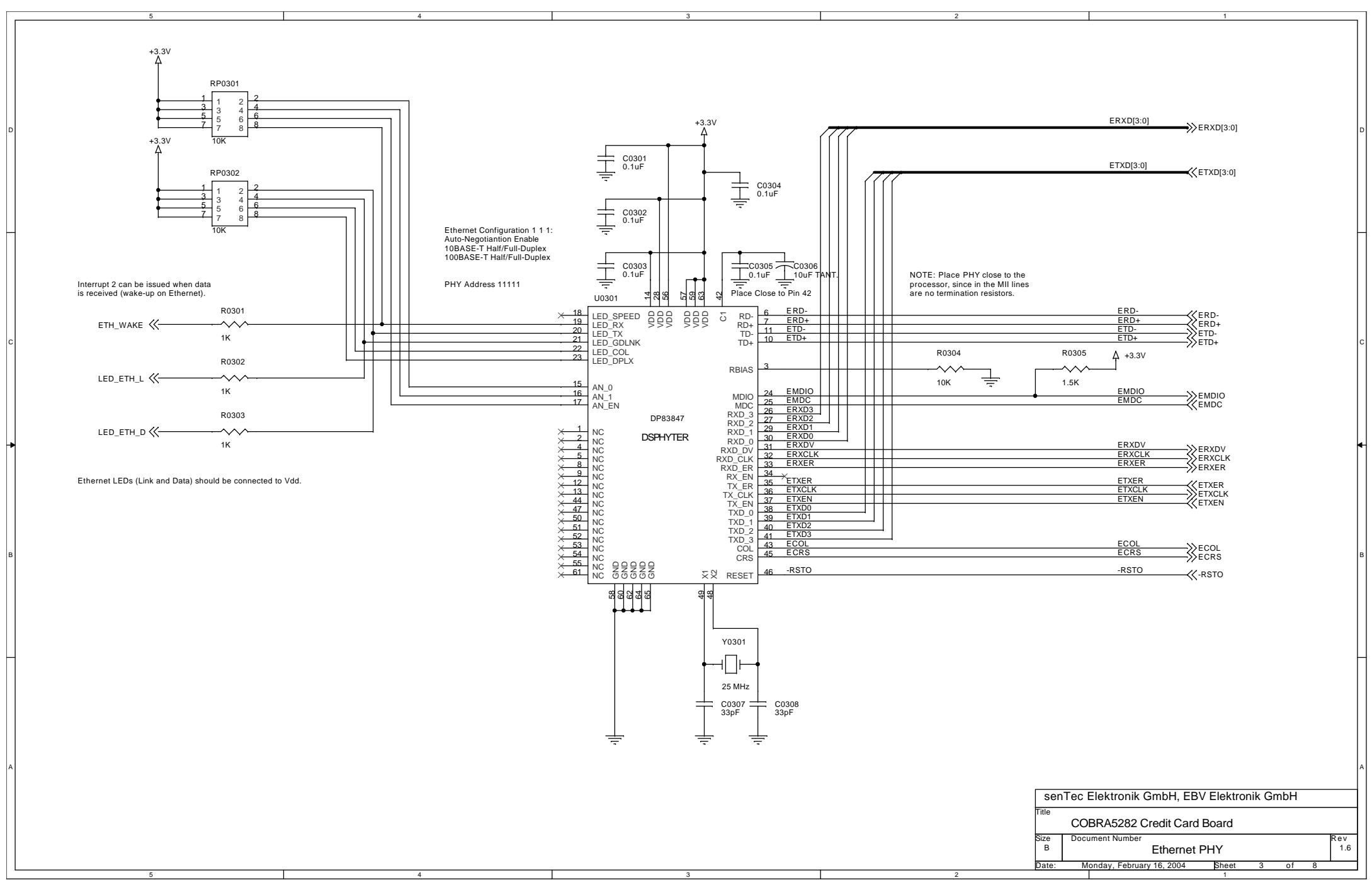
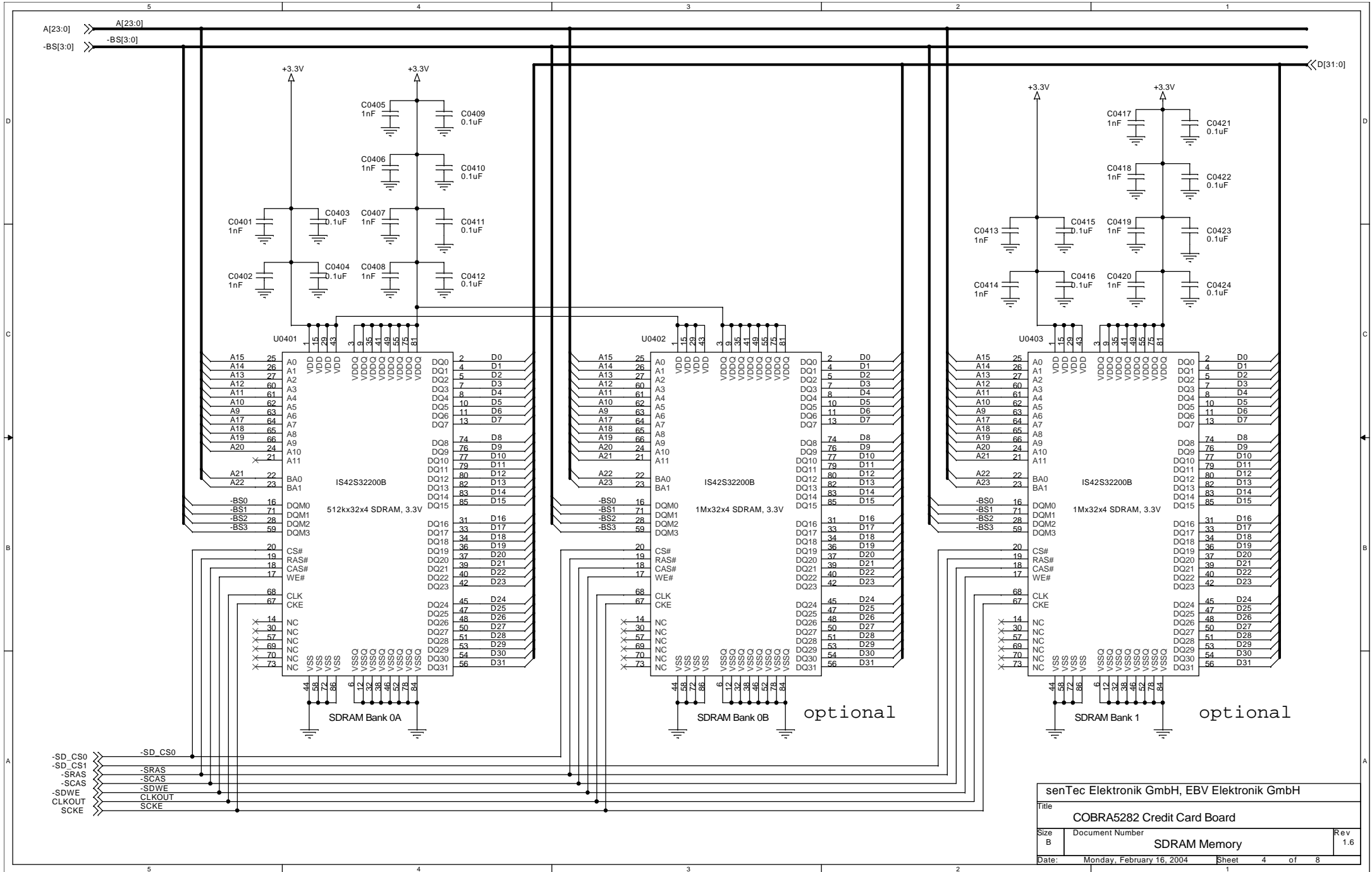
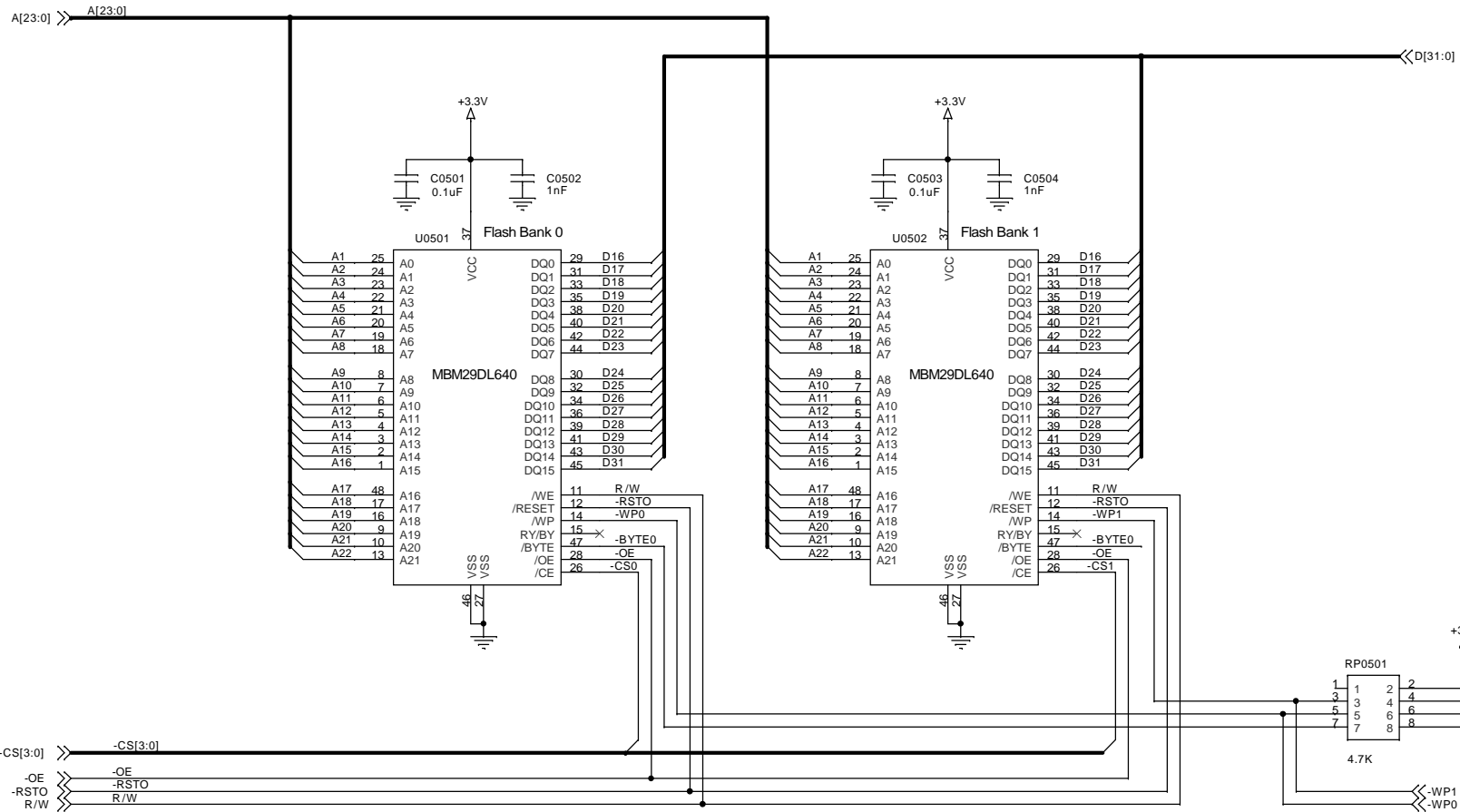


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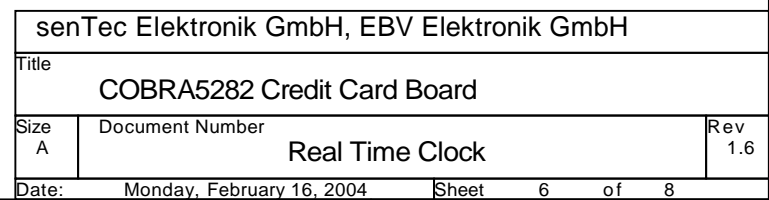








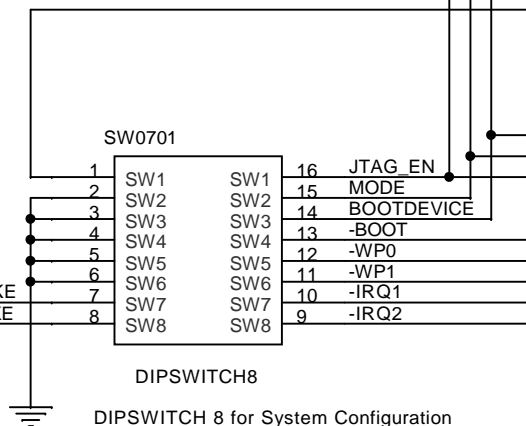
Flash is write protected when signals
-WP0 and -WP1 are tied to ground.



Chip Configuration:

-RCON: fixed: L (Chip Configuration ON)
 JTAG_EN: switched: L=BDM; H=JTAG (default = L)
 CLKMOD0: fixed: H (PLL with Crystal)
 CLKMOD1: fixed: H (PLL with Crystal)
 D26: fixed: H
 D17: fixed: H
 D16: switched: H=Master Mode, L=Single Chip Mode (default = H)
 D19: fixed: L
 D18: switched: H=External 16 Bit, L=Internal Boot (default = H)
 D21: fixed: H (full drive, can be reconfigured via software)
 D25: fixed: L (A[23:21])
 D24: fixed: L (A[23:21])

RTC_WAKE
 ETH_WAKE

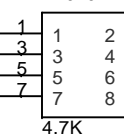


DIPSWITCH8

DIPSWITCH 8 for System Configuration
 SW1: OFF: BDM ON: JTAG
 SW2: OFF: Master Mode ON: Single Chip Mode
 SW3: OFF: External Boot ON: Internal Boot
 SW4: OFF: Boot Option 1 ON: Boot Option 2
 SW5: ON: Flash Bank 0 Write Protected
 SW6: ON: Flash Bank 1 Write Protected
 SW7: ON: Wake Up on RTC (-IRQ1)
 SW8: ON: Wake Up on Ethernet (-IRQ2)

+3.3V

RP0702

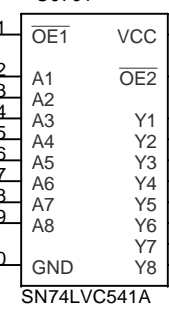


JTAG_EN

-RCON
 CLKMOD0
 CLKMOD1
 BOOTOPTION

-RSTO

U0701

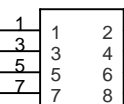


SN74LVC541A

+3.3V

C0701
 0.1uF

RP0701



-WP0
 -WP1

-IRQ[7:1]

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