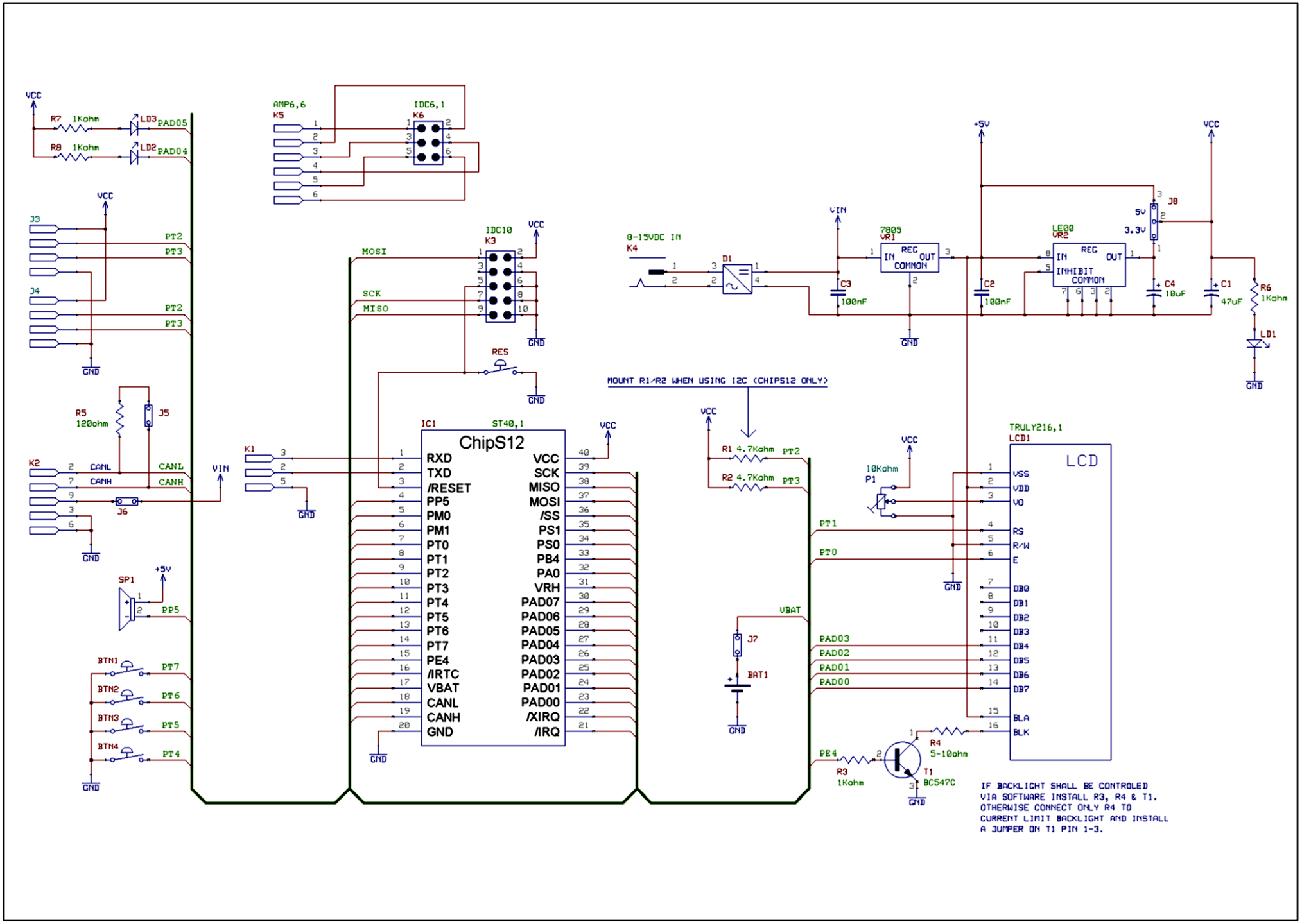
VCC
R7 1Kohm
LD3
PAD05
R8 1Kohm
LD2
PAD04
J3
PT2
PT3
J4
PT2
PT3
GND
AMP6,6
K5
IDC6,1
K6
IDC10
K3
MOSI
SCK
MISO
RES
R5
120ohm
J5
K2
CANL
CANH
J6
VIN
K1
SP1
+5V
PP5
BTN1
BTN2
BTN3
BTN4
PT7
PT6
PT5
PT4
IC1
ST40,1
ChipS12
RXD
TXD
/RESET
PP5
PM0
PM1
PT0
PT1
PT2
PT3
PT4
PT5
PT6
PT7
PE4
/IRTC
VBAT
CANL
CANH
GND
VCC
SCK
MISO
MOSI
/SS
PS1
PS0
PB4
PA0
VRH
PAD07
PAD06
PAD05
PAD04
PAD03
PAD02
PAD01
PAD00
/XIRQ
/IRQ
8-15VDC IN
K4
D1
C3
100nF
7805
VR1
REG
IN
OUT
COMMON
C2
100nF
LE00
VR2
INHIBIT
5V
3.3V
J8
C4
10uF
C1
47uF
R6
1Kohm
LD1
MOUNT R1/R2 WHEN USING I2C (CHIPS12 ONLY)
R1 4.7Kohm
R2 4.7Kohm
PT2
PT3
10Kohm
P1
PT1
PT0
VBAT
J7
BAT1
PAD03
PAD02
PAD01
PAD00
PE4
R3
1Kohm
R4
5-10ohm
T1
BC547C
TRULY216,1
LCD1
LCD
VSS
VDD
VO
RS
R/W
E
DB0
DB1
DB2
DB3
DB4
DB5
DB6
DB7
BLA
BLK
IF BACKLIGHT SHALL BE CONTROLED
VIA SOFTWARE INSTALL R3, R4 & T1.
OTHERWISE CONNECT ONLY R4 TO
CURRENT LIMIT BACKLIGHT AND INSTALL
A JUMPER ON T1 PIN 1-3.